

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Kimikatsu SHOJI et al.

Appln. No. 09/377,740

Filed: August 20, 1999

For: SEMICONDUCTOR
INTEGRATED CIRCUIT AND
METHOD OF MANUFACTURING
THE SAME



Art Unit: 2811

Examiner: Sara W. CRANE

Atty. Docket No. 32014-150502

Customer No.



26694

PATENT TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

FEB 13 2003

RECEIVED

#21
Election
3/5/03
ars

Response to Restriction Requirement

Honorable Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Restriction Requirement in the Office Action dated January 13, 2003, Applicants hereby elect Group I, represented by claims 15-19 and 24-26. An examination of the merits of claims 15-19 and 24-26 is respectfully requested.

Respectfully submitted,

A handwritten signature in cursive script that reads "Chad C. Anderson".

Chad C. Anderson

Registration No. 44,505

VENABLE

P.O. Box 34385

Washington, D.C. 20043-9998

Telephone: (202) 962-4800

Telefax: (202) 962-8300

Date: 02/12/03